



University College Sedaya International



Faculty Of Engineering & Technology

School Of Engineering

FINAL YEAR PROJECT

FINAL REPORT

THE DESIGN OF PLL FM DEMODULATOR

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Abstract

This is the complete final report of the project, The Design of PLL FM Demodulator. In short this project required the student to build a PLL FM Demodulator and to understand the operation of the PLL as a FM demodulator. Both the theoretical and the mathematical equation of the PLL need to be studied and understood. Other than PLL FM Demodulator, other types of FM Demodulators are also studied and understood. This report contains necessary information related to the project. In order for the readers' convenience, this report is divided into several chapters. These chapters include the introduction, basic theory, hardware development, simulations results, experimental results and discussions, and project management. Information in each chapter is explained in details.

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