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FACULTY OF ENGINEERING AND INFORMATION

TECHNOLOGY

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FINAL REPORT

FINAL YEAR ENGINEERING PROJECT 2002

Project title	: DESIGN OF MICROWAVE MULTIPLIER
Name	: TONG WHYE MENG
Student I.D.	: 99005350
First supervisor	: MR. LOW
Second supervisor	: DR. KHEDR
Project Coordinator	: DR. KHEDR





ABSTRACT

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Many radio transmitters and communication devices use some form of multiplier to raise the low frequency signal developed by the oscillator to a high frequency. This report presents the design, analysis and fabrication of microwave multiplier implemented in microstrip substrates for narrowband. The multiplication factor desired is 2, for an input frequency of 1.2 GHz to generate an output of 2.4 GHz. Various techniques are analysed and presented in theory and practical simulations using Microwave Office 2002.

The result of the project would be a fully functional, microwave frequency multiplier according to the specific parameters chosen.