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FACULTY OF ENGINEERING AND INFORMATION TECHNOLOGY SCHOOL OF ENGINEERING

FINAL REPORT

FINAL YEAR ENGINEERING PROJECT 2002

Project Title

: ADC SIMULATION USING MATLAB

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Abstract

Analog-to-Digital Converter (ADC) are widely used in data communication and multimedia application. Recent developments in communication have resulted in high speed voice and data channels. The speed of an ADC is critical in these applications.

ADC Simulation using MATLAB is the project that to produce the digital signal with the analog input signal. This project also investigates the different parameters of ADC. MATLAB version 6.1 in-cooperates with its SIMULINK is implemented into this project. By using the DSP concept, the parameters will be investigated after simulate the ADC system using block set in the SIMULINK.

The overall aim of this project is to study and know the theory of the ADC. From this project, students will be tough in the functions, operations, characteristics and the parameters of different type of ADC.