

School of Engineering
Engineering Project (3317)

DESIGN OF PHASE LOCKED LOOP
FINAL REPORT

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JANUARY - AUGUST 2005

Abstract

The objective of this project is to develop a phase locked loop (PLL) system which is capable of locking onto or synchronizing with an incoming signal. PLL is just an electronic circuit with voltage or current driven oscillator that is constantly adjusted to match in phase the frequency of an input signal. Phase locked loop consist of three major elements that is phase comparator that acts as a mixer, voltage controlled oscillator and loop filter. PLL have been used in wide variety of communication system. This report explains the researches undertaken, theory and applications for developing this system. Final product results included for discussion.